

## GENERAL INFORMATION

<b>SIMPL WINDOWS NAME</b>	Intelix FLX-88 demo.smw
<b>CATEGORY</b>	Video switching
<b>VERSION</b>	1.0.0.2
<b>SUMMARY</b>	Controls the Intelix FLX-88 via RS-232 or IP.
<b>GENERAL NOTES</b>	Controls routing of sources to destinations based on analog inputs. Provides polling routine with adjustable timing. Provides EDID management including reset and copying from outputs. Provides preset management including save, reset, and clear. Provides standby control. Provides route-through and deroute all functions. Provides front panel lock out. Provides asynchronous feedback message control. Provides per-input HDCP compliance control. Provides per-input input signal type control. Provides per-input VGA auto-adjust. Provides per-input scaling controls. Provides per-input HDMI audio embedding controls. Provides true feedback.
<b>CRESTRON HARDWARE REQUIRED</b>	2- or 3-SERIES PROCESSORS
<b>COMMUNICATION SETUP</b>	RS232: 9600 Baud, No Parity, 8 Data Bits, 1 Stop Bit TCP/IP: Port 4001
<b>VENDOR FIRMWARE</b>	Pro2: 4.007.0004 MC3: 1.005.0007 FLX-88: 1.6.4

## CONTROL

<b>rx</b>	S	Connect to serial or IP receive input
<b>Output_[1-8]</b>	A	Set to input number to route to corresponding output
<b>Reset_Matrix_EDID</b>	D	Set all inputs to factory EDID settings
<b>Input_[1-8]_EDID_From_Output</b>	A	Set output from which to copy EDID for specified input
<b>Recall_Preset_[0-9]</b>	D	Pulse to recall matrix preset
<b>Save_Preset_[0-9]</b>	D	Pulse to save matrix preset
<b>Clear_Preset_[0-9]</b>	D	Pulse to clear/erase stored matrix preset
<b>Power_On</b>	D	Activate matrix power

<b>Power_Off</b>	D	Put matrix into standby mode
<b>Route_All_Inputs_Through</b>	D	Pulse to route input 1 to output 1, 2 to 2, etc.
<b>Unroute_All_Outputs</b>	D	Pulse to unroute all outputs
<b>Front_Panel_Lock</b>	D	Pulse to lock front panel
<b>Front_Panel_Unlock</b>	D	Pulse to unlock front panel
<b>Feedback_Messages_On</b>	D	Pulse to enable front panel routing feedback
<b>Feedback_Messages_Off</b>	D	Pulse to disable front panel routing feedback
<b>Poll_Enable</b>	D	Enable true feedback polling at the rate specified by "poll rate"
<b>Input_[1-8]_HDCP_Compliance_On</b>	D	Pulse to enable HDCP compliance on specified input
<b>Input_[1-8]_HDCP_Compliance_Off</b>	D	Pulse to disable HDCP compliance on specified input
<b>Input_[1-8]_FLX-RI4_Signal_Type_VGA</b>	D	Pulse to set FLX-PI4 input for VGA signal
<b>Input_[1-8]_FLX-RI4_Signal_Type_Component</b>	D	Pulse to set FLX-PI4 input for Component signal
<b>Input_[1-8]_FLX-RI4_Signal_Type_S-Video</b>	D	Pulse to set FLX-PI4 input for S-Video signal
<b>Input_[1-8]_FLX-RI4_Signal_Type_Composite</b>	D	Pulse to set FLX-PI4 input for Composite signal
<b>Input_[1-8]_FLX-RI4_VGA_Auto-Adjust</b>	D	Pulse to auto-adjust FLX-RI4 input in VGA mode
<b>Input_[1-8]_FLX-RI4_Scaler_Resolution_1024x768_(XGA)</b>	D	Pulse to set FLX-RI4 input to be scaled to XGA resolution
<b>Input_[1-8]_FLX-RI4_Scaler_Resolution_1280x720_(720p)</b>	D	Pulse to set FLX-RI4 input to be scaled to 720p resolution
<b>Input_[1-8]_FLX-RI4_Scaler_Resolution_1280x800_(WXGA)</b>	D	Pulse to set FLX-RI4 input to be scaled to WXGA resolution
<b>Input_[1-8]_FLX-RI4_Scaler_Resolution_1920x1080_(1080p)</b>	D	Pulse to set FLX-RI4 input to be scaled to 1080p resolution
<b>Input_[1-8]_FLX-HI4A_Audio_Source_Digital_(HDMI)</b>	D	Pulse to capture digital audio for specified FLX-HI4A input

<b>Input_[1-8]_FLX-HI4A_Audio _Source_Analog -(Euroblock)</b>	D	Pulse to capture analog audio for specified FLX-HI4A input
---	---	--

## PARAMETERS

<b>Poll Rate</b>	P	How long to wait between polling cycles
------------------	---	---

## FEEDBACK

<b>tx</b>	S	Connect to serial or IP transmit output
<b>Output_[1-8]_fb</b>	A	True feedback, input currently routed to specified output
<b>Power_On_fb</b>	D	True feedback, high when unit reports on
<b>Power_Off_fb</b>	D	True feedback, high when unit reports standby
<b>Front_Panel_Lock_fb</b>	D	True feedback, high when unit reports front panel locked
<b>Front_Panel_Unlock_fb</b>	D	True feedback, high when unit reports front panel unlocked
<b>Feedback_Messages_On_fb</b>	D	True feedback, high when unit reports front panel routing feedback enabled
<b>Feedback_Messages_Off_fb</b>	D	True feedback, high when unit reports front panel routing feedback disabled